



GP
ELECTRONICS

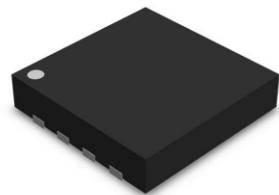
GP20N07D33

20V N-Channel MOSFET

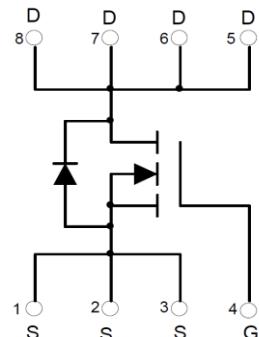
Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
20V	5.5mΩ@4.5V	25A
	6.0mΩ@4.0V	
	6.5mΩ@3.8V	
	7.0mΩ@3.1V	
	7.5mΩ@2.5V	

DFN3X3-8L



Schematic diagram



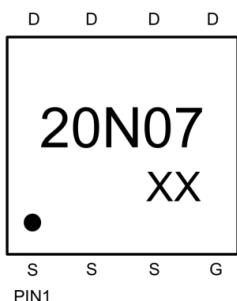
Feature

- High cell density trenched N-ch MOSFETs
- Super low gate charge
- Advanced high cell density Trench technology

Application

- Battery protection applications
- Load switch

Marking:



20N07 = Device code

XX = Date Code

Solid dot = Pin1 indicator

PIN1

ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ\text{C}$ unless otherwise noted)

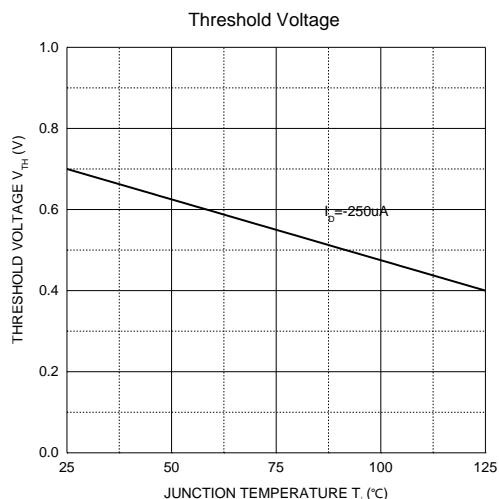
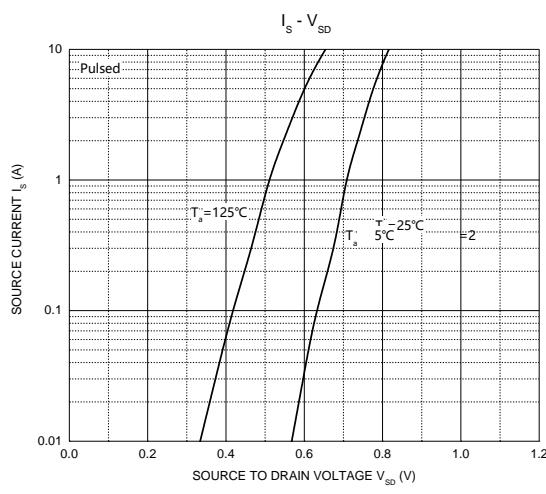
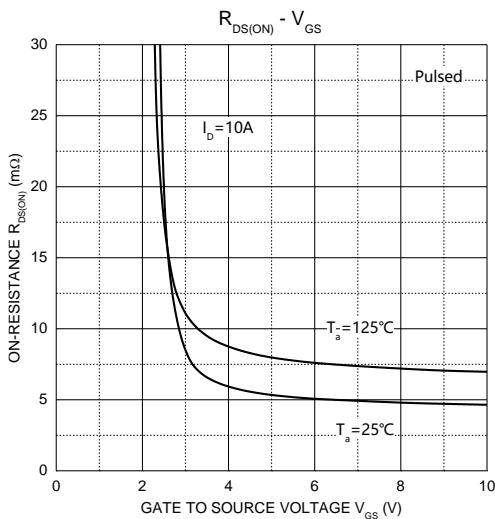
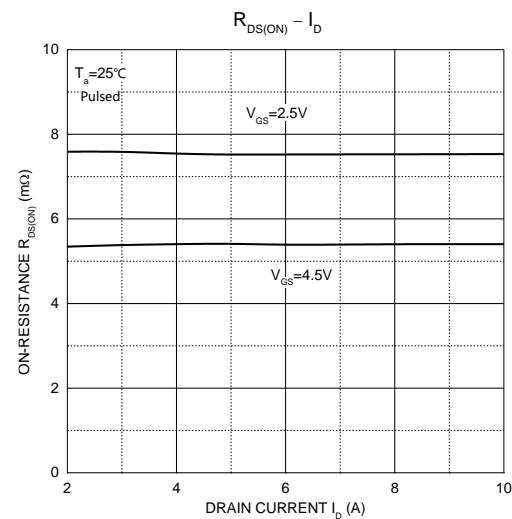
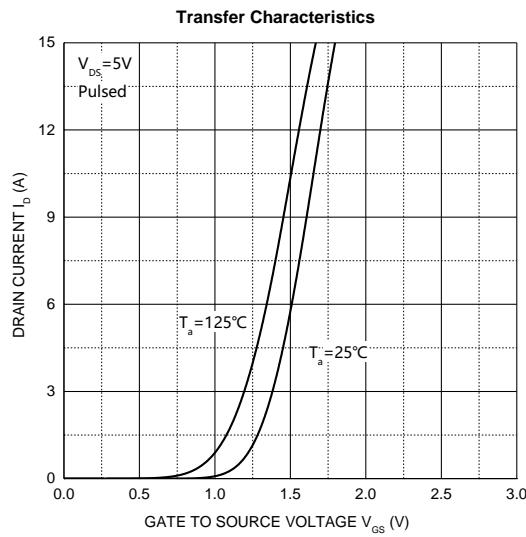
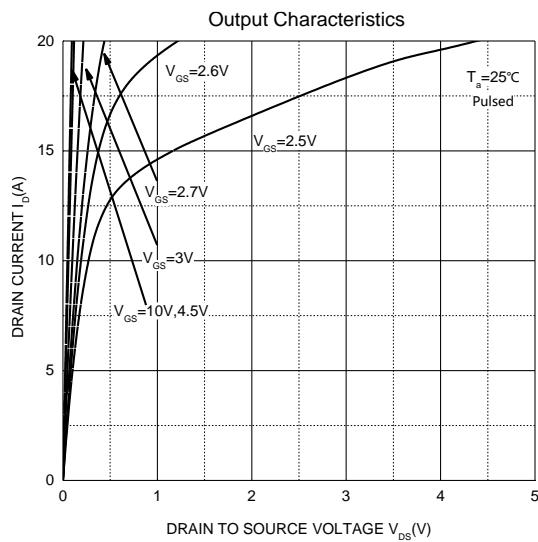
Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current	$I_D^{(1)}$	25	A
Pulsed Drain Current	$I_{DM}^{(1), 2)}$	75	A
Power Dissipation	$P_D^{(3)}$	3	W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	42	°C/W
Junction Temperature	T_J	150	°C
Storage Temperature	T_{STG}	-55~+150	°C

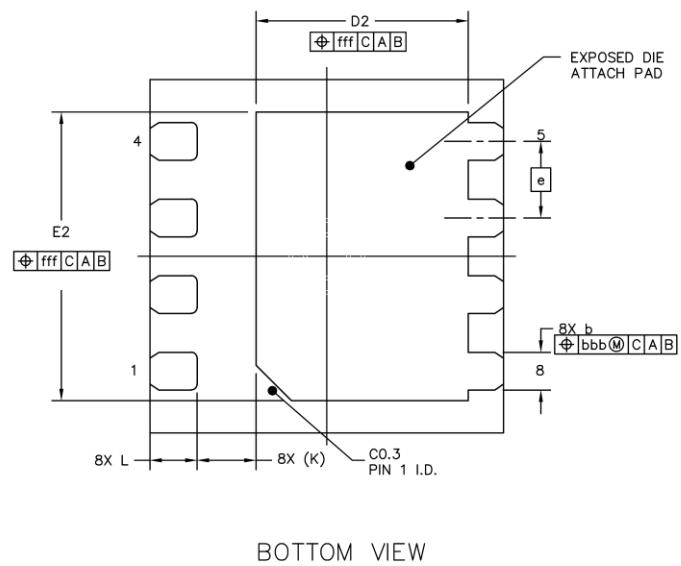
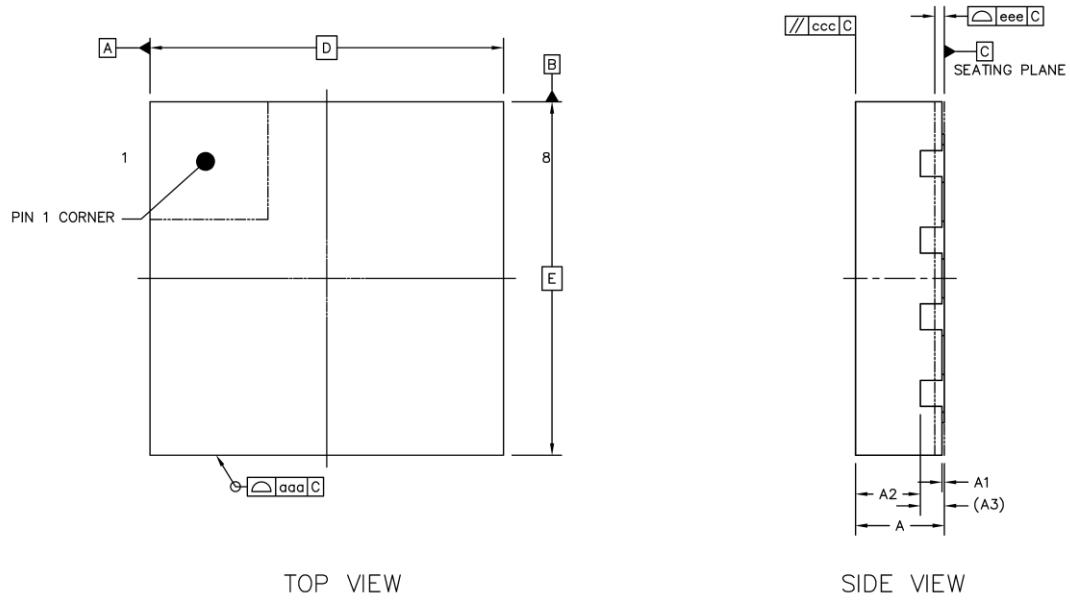
MOSFET ELECTRICAL CHARACTERISTICS ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Static Characteristics						
Drain-source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0V, I_D = 250\mu\text{A}$	20			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 16\text{V}, V_{GS} = 0\text{V}$			-1	μA
Gate-body Leakage Current	I_{GSS}	$V_{GS} = \pm 12\text{V}, V_{DS} = 0\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{GS(\text{th})}^{(4)}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	0.4	0.7	1.0	V
Drain-source On-resistance	$R_{DS(\text{on})}^{(4)}$	$V_{GS} = 4.5\text{V}, I_D = 10\text{A}$		5.5	7.0	$\text{m}\Omega$
		$V_{GS} = 4.0\text{V}, I_D = 10\text{A}$		6.0	7.5	
		$V_{GS} = 3.8\text{V}, I_D = 10\text{A}$		6.5	8.0	
		$V_{GS} = 3.1\text{V}, I_D = 10\text{A}$		7.0	9.0	
		$V_{GS} = 2.5\text{V}, I_D = 10\text{A}$		7.5	10.0	
Dynamic characteristics⁽⁵⁾						
Input Capacitance	C_{iss}	$V_{DS} = 10\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$		1500		pF
Output Capacitance	C_{oss}			260		
Reverse Transfer Capacitance	C_{rss}			240		
Switching Characteristics⁽⁵⁾						
Total Gate Charge	Q_g	$V_{DS} = 10\text{V}, V_{GS} = 4.5\text{V}, I_D = 8\text{A}$		20		nC
Gate-source Charge	Q_{gs}			4		
Gate-drain Charge	Q_{gd}			9		
Turn-on Delay Time	$t_{d(on)}$	$V_{GS} = 10\text{V}, V_{DS} = 10\text{V}, R_L = 1.2\Omega, R_{\text{GEN}} = 3\Omega$		5		ns
Turn-on Rise Time	t_r			15		
Turn-off Delay Time	$t_{d(off)}$			70		
Turn-off Fall Time	t_f			22		
Diode Characteristics						
Continuous Source Current	I_s	$V_G = V_D = 0\text{V}$, Force Current			25	A
Pulsed Source Current	I_{SM}				75	
Diode Forward Voltage	$V_{SD}^{(4)}$	$V_{GS} = 0\text{V}, I_s = 10\text{A}, T_A = 25^\circ\text{C}$			1.2	V

Notes:

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper
- 2.Pulse Test:Pulse Width < 10us, Duty Cycle < 0.5%.
- 3.The power dissipation is limited by 150°C junction temperature
- 4.Pulse Test : Pulse width≤300μs, duty cycle≤0.5%.
- 5.Guaranteed by design, not subject to production testing.
- 6.The data is theoretically the same as ID, in real applications , should be limited by total power dissipation.

Typical Electrical and Thermal Characteristics


DFN3X3-8L Package Information


DFN3X3-8L Package Information

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700	0.800	0.028	0.031
A1	0.000	0.050	0.000	0.002
A2	0.550TYP		0.022TYP	
A3	0.203REF		0.008REF	
b	0.270	0.370	0.011	0.015
D	3.000BSC		0.118BSC	
E	3.000BSC		0.118BSC	
e	0.650BSC		0.026BSC	
D2	1.700	1.900	0.067	0.075
E2	2.350	2.550	0.093	0.100
L	0.300	0.500	0.012	0.020
K	0.500REF		0.020REF	
aaa	0.100TYP		0.004TYP	
ccc	0.100TYP		0.004TYP	
eee	0.080TYP		0.003TYP	
bbb	0.100TYP		0.004TYP	
fff	0.100TYP		0.004TYP	